

Notice of References Cited	Application/Control No. 09/712,420	Applicant(s)/Patent Under Reexamination DANEMAN ET AL.	
	Examiner Roberts Culbert	Art Unit 1763	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,426,070	06-1995	Shaw et al.	216/2
	B	US-5,770,465	06-1998	MacDonald et al.	216/2
	C	US-5,084,419	01-1992	Sakao, Masato	438/400
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	S. Wolf, "Silicon Processing for the VLSI Era", Vol. 4, pp. 14-15, Lattice Press, 2002
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.